

DYNAMICALLY SETTING THE OPTIMAL BASE ADDRESSES OF PROCESS COMPONENTS

Abstract of the Disclosure

[028] Processes are monitored to determine if all of their components are loaded from persistent storage into memory at their preferred base addresses. Each of the components is examined to determine if that component's in-memory base address matches the preferred base address of its on-disk representation. If a base address collision is detected, the on-disk representation of the preferred base address is updated to reflect the new in-memory base address.